

which is arbitrarily given. Accordingly, in the system in which the amplitude is fluctuated, the stable adaptive equalization operation can be realized without changing over the reference value for computing the equalization error based on the amplitude. Further, by interlocking the reference value with the threshold value of the binarization circuit which constitutes the rear stage of the equalizer circuit, the equalization characteristics which exhibit the stability to the displacement of the symmetry of the input signal can be realized.